What is claimed is:

1. A system comprising:

a process comprising multiple-mapped memory;

a first set of memory mapped onto the multiple-mapped memory,

a second set of memory mapped onto the multiple-mapped memory; and

an address overload circuit to selectively map the multiple-mapped memory to the first set of memory or to the second set of memory.

2. A system as defined in Claim 1, wherein the second set of memory comprises instructions that are effective to execute a protected function.

3. A system as defined in Claim 2, further comprising a transfer agent to receive parameters from the process and to assume control of execution of the process when the multiple-mapped memory is mapped to a protected set of memory.

4. A system as defined in Claim 3, wherein the transfer agent is effective to call a protected function.

5. A system as defined in Claim 4, wherein the transfer agent is effective to call the protected function using parameters received from the process.

6. A system as defined in Claim 4, wherein the transfer agent is stored on nonvolatile memory.

7. A system as defined in Claim 6, wherein the transfer agent executes on internal memory.

8. A system as defined in Claim 3, wherein the address overload circuit comprises:

(a) an address multiplexer;

(b) an address translator coupled to the address multiplexer; and

(c) a data multiplexer.

9. A method comprising:

executing a process that comprises multiple-mapped memory;

determining whether the process is a trusted process;

if the process is determined not to be a trusted process, mapping the multiple-mapped memory to unprotected memory; and

if the process is determined to be a trusted process, mapping the multiple-mapped memory to protected memory.

10. A method as defined in Claim 9, further comprising:

storing a transfer agent.

11. A method as defined in Claim 10, wherein the transfer agent is stored in a first memory.

12. A method as defined in Claim 10, further comprising:

determining that the process is a trusted process;

copying the transfer agent to a second memory;

transferring parameters from the process to the transfer agent; and

controlling execution of the process with the transfer agent.

13. A method as defined in Claim 12, further comprising:

executing the transfer agent so as to identify a protected function and to call the protected function.

14. A method as defined in Claim 13, further comprising:

executing the protected function.

15. A method as defined in Claim 12, further comprising:

operating a trust co-processor to determine whether the process is a trusted process.

16. A method as defined in Claim 15, further comprising:

executing the transfer agent so as to identify a protected function and to call the protected function.

17. A method as defined in Claim 16, further comprising:

executing the protected function.

18. An article comprising a machine-readable storage medium on which there are stored instructions that, if executed, enable a system to:

determine whether a process is a trusted process; and

if the process is a trusted process, transfer, at least temporarily, control of the process to a transfer agent.

19. An article as defined in Claim 18, wherein instructions, if executed, enable the system to transfer process parameters to the transfer agent.

20. An article as defined in Claim 19, wherein the instructions, if executed, enable the system to identify and execute a protected function.

21. An article as defined in Claim 20, wherein the instructions, if executed, enable the system to copy the transfer agent from nonvolatile memory to volatile memory in the course of executing multiple-mapped memory.

22. An article as defined in Claim 18, wherein the instructions, if executed, enable the system to determine whether a process is a trusted process in response to the detection of multiple-mapped memory.

23. An article as defined in Claim 22, wherein the instructions, if executed, enable the system to:

determine that a process is a trusted process;

transfer, at least temporarily, control of the process to the transfer agent; and

transfer process parameters to the transfer agent.

24. An article as defined in Claim 23, wherein the instructions, if executed, enable the system to:

by operation of the transfer agent, identify , call, and execute a protected process.

25. An article as defined in Claim 24, wherein the instructions, if executed, enable the system to copy the transfer agent from nonvolatile memory to volatile memory in the course of executing a trusted process that comprises multiple-mapped memory.

26. A system comprising:

an integrated circuit device comprising a processor, internal random access memory (RAM), and internal read only memory (ROM);

unprotected memory;

protected memory;

a process to execute on the internal RAM, the process comprising multiple-mapped memory, the multiple-mapped memory to be selectively mapped to either the protected memory or the unprotected memory;

a trust co-processor to determine whether the multiple-mapped memory is to be mapped to the unprotected memory or is to be mapped to the protected memory;

a wireless interface coupled to the processor; and

an antenna coupled to the wireless interface.

27. A system as defined in Claim 26, further comprising a circuit coupled to the trust co-processor to selectively map the multiple-mapped memory to the protected memory.

28. A system as defined in Claim 27, wherein the circuit comprises:

(a) an address multiplexer;

(b) an address translator coupled to the address multiplexer; and

(c) a data multiplexer.

29. A system as defined in Claim 26, further comprising a transfer agent to receive parameters from a trusted process, call a protected function using the parameters, and cause the protected function to execute.

30. A system as defined in Claim 29, further comprising a circuit coupled to the trust co-processor to selectively map the multiple-mapped memory to the protected memory.